# MICRO-SCALE ENERGY HARVESTING

## Chao Lu, Vijay Raghunathan and Kaushik Roy

Department of Electrical and Computer Engineering, Purdue University, USA

**Keywords:** Micro scale, energy harvesting, maximum power point, energy transducer, power converter, tracking

## Contents

- 1. Introduction
- 2. Energy Transducer Characterization
- 2.1. Micro Photovoltaic Module
- 2.2. Micro Thermoelectric Generator
- 2.3. Micro Fuel Cell
- 2.4. Micro Vibration Energy Transducer
- 2.5. Micro Electromagnetic Energy Transducer
- 3. Power Converter
- 3.1. AC/DC Rectifier
- 3.2. DC/DC Converter
- 3.3. MIMO Power Converter
- 4. MPP Tracking Approaches
- 4.1. Design Time Component Matching (DTCM)
- 4.2. Reference Voltage Tracking
- 4.3. Hill-Climbing / Perturb and Observe (P&O)
- 4.4. MPP Tracking for Hybrid Transducers
- 5. Harvesting-aware Application Unit
- 6. Conclusion
- Glossary
- Bibliography
- **Biographical Sketches**

## Summary

Environmental energy sources abound in our immediate surroundings. Energy harvesting is a physical process by which the energy is collected from the environment. Examples of such energy sources include light, thermal gradients, vibrations, electromagnetic wave, etc. Harvesting electrical power from environmental energy sources is an attractive and increasingly feasible option for several micro-scale electronic systems such as biomedical implants and wireless sensor nodes that need to operate autonomously for long periods of time (months to years).

However, designing highly efficient micro-scale energy harvesting systems requires an in-depth understanding of various design considerations and tradeoffs. This book chapter provides an overview of the area of micro-scale energy harvesting and discusses the various challenges and considerations involved from a design perspective.

## **1. Introduction**

As the world is more and more concerned with fossil fuel exhaustion and environmental problems caused by conventional power generation, renewable resources are becoming a focal point of the environmental movement, both politically and economically. Environmental energy sources abound in our immediate surroundings. Energy harvesting is a physical process by which the energy is collected from the environment. Examples of such energy sources include light, thermal gradients, vibrations, electromagnetic wave, etc. Harvesting energy from the surrounding environment is of growing interest to the research community, but in practice, design challenges limits its viability and ability to penetrate the market.

Nowadays, rapid advances in computing, communication, and integration has resulted in the emergence of a new class of ultra-low power applications. Examples of such systems include wearable or implantable biomedical devices [Yazicioglu 2009], wireless sensor nodes [Raghunathan 2004], etc. These systems are often required to operate for several months to years without the need of battery replacement, because frequent battery replacement may be infeasible (e.g., for biomedical implants) or prohibitively expensive (e.g., in a large sensor network). Energy storage element, e.g. battery, is extensively used for powering electronic systems. However, since the volume permitted for battery integration in these miniaturized systems is quite tiny (and hence very limited energy capacity), the energy storage element will be quickly depleted after a short time of system operating and these systems will become useless. Frequent battery replacement is impractical in these micro systems, since it is prohibitively expensive for large wireless sensor network that consist of hundreds to thousands of spatially distributed autonomous micro-sensor nodes, or it often requires invasive surgery (e.g., pacemaker batteries need to be replaced every six to seven years, on average). Loss of power in a biomedical implant due to a depleted energy storage element can have serious and potentially life threatening consequences. As a result, one key challenge in these systems is to conveniently provide the required power for longlived, maintenance-free operation.

Environmental energy harvesting is an attractive option to alleviate the power supply challenge in these systems [Mateu 2005; Raghunathan 2005]. Examples of ambient energy sources are light, thermal, fuel, vibration, radio frequency waves, etc. While the basic idea of environmental energy harvesting has been extensively explored and applied at the macro-scale in the context of large systems such as solar farms, windmills, etc., designing micro-scale energy harvesting systems involves several new challenges. Most of these challenges stem from the fact that the form-factor constraint in these systems mandates the use of miniature energy transducers (a few cm<sup>3</sup>). As a result, the maximum power output of these micro-scale transducers is extremely small, often only a few mW. Therefore, the harvesting subsystem should be carefully designed to extract as much power as possible from the energy transducer and transfer it to the electronic system with minimal loss, which requires extremely energy efficient design techniques. Energy harvesting is an alternative method of providing power to these micro systems and has the potential to result in perpetual operation. This book chapter presents an overview of the various circuit design considerations and techniques involved in designing energy-efficient micro-scale energy harvesting systems.

In addition, energy harvesting also provides significant environmental benefits. For example, the large number of batteries discarded in solid waste landfills represents a long-term threat to groundwater and drinking water supplies due to heavy metal (e.g., mercury, cadmium) leakage. The use of energy harvesting in micro systems significantly prolongs overall battery life and in some cases, eliminates the dependence on batteries, and thus, directly contributes towards mitigating this problem.

Figure 1 shows the generic block diagram of a micro-scale energy harvesting system. It consists of five blocks: the micro scale energy transducer, the power converter, the control unit, the energy buffer, and the application unit. The energy transducer converts ambient energy into electrical energy, which is stored in the energy buffer (a rechargeable battery or a super capacitor) for powering the application unit (e.g., sensor node or biomedical implant). The energy transducer may be based on one energy conversion mechanism or a hybrid heterogeneous combination. The control unit plays a crucial role in maximizing overall system efficiency. It produces the required control signals for the entire system and ensures maximum power point (MPP) operation at all times by running a MPP tracking scheme. The goal of the power converter is to extract as much power ( $P_{\rm S}$ ) as possible from the energy transducer and pass on as much of it as possible ( $P_{\rm EB}$ ) to the output. In this chapter, each building block will be addressed and discussed in the following sections.

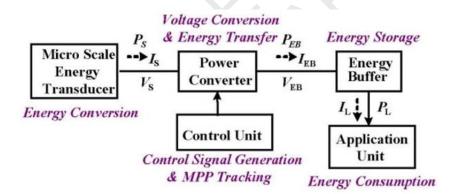


Figure 1. Block diagram of a micro scale energy harvesting system

The rest of this chapter is organized as follows. In Section 2, we briefly review the basic device physics and characterize the electrical behavior of various energy transducers. Design considerations and research progresses for energy-efficient power converters are introduced in Section 3. In order to enhance the charge transfer capability from ultra low voltage energy transducers, a tree topology charge pump is analyzed and discussed thoroughly. In Section 4, the previously proposed MPP tracking approaches are classified and addressed, followed by a discussion of harvesting-aware application unit design in Section 5. Finally, the conclusion is given in Section 6.

## 2. Energy Transducer Characterization

Environmental energy sources are ubiquitous in our immediate surroundings. Examples of such energy sources include solar radiation, air flow, mechanical motion/vibration,

thermal gradients, radio frequency (RF) transmissions, etc. A variety of micro-scale energy transducers have been developed to convert energy from other modalities into electrical energy [Choi 2006; Chu 2006; Egbert 2007]. The dominant characteristic of energy transducers is their power density (Watt/cm<sup>3</sup>). This is because transducers will never run out of energy (barring any hardware failures) as long as the environmental energy source is present, and hence, cannot be viewed as conventional capacity-limited energy sources (i.e., battery).

Table 1 shows the estimated power densities of a few commonly used energyharvesting modalities [Raghunathan 2005]. While there has been (continues to be) extensive research from the device perspective to improve the cost, conversion efficiency, and power density of transducers, it is crucial for system designers to be aware of their electrical characteristics in-depth in order to understand their impact on the system being powered. Although various physical or mathematical models have been proposed to characterize micro scale energy transducers, these models are cumbersome, computationally intensive, and incompatible with circuit design or simulation software (e.g. Cadence or SPICE). Hence, in the remainder of this section, we provide an overview of various energy-harvesting modalities and describe how some of these transducers can be modeled from electrical perspective.

Harvesting technology	Power density
Solar cells (outdoors at noon)	15mW/cm <sup>3</sup>
Piezoelectric (shoe inserts)	$330\mu$ W/cm <sup>3</sup>
Vibration (small microwave oven)	116µW/cm <sup>3</sup>
Thermoelectric (10°C gradient)	$40\mu$ W/cm <sup>3</sup>
Acoustic noise (100dB)	960nW/cm <sup>3</sup>

Table 1. Power densities of various energy harvesting modalities

## 2.1 Micro Photovoltaic Module

A photovoltaic (PV) cell is a device that converts the light energy directly into electricity by the photovoltaic effect. It is useful to create an electrically equivalent model that is SPICE-compatible. This model facilitates the design of remaining system building blocks (e.g. power converter or control unit) and enables system-level simulations and verification. This SPICE-compatible model allows system designers to simulate and observe the matching status between a power converter and an energy transducer.

Figure 2 shows the equivalent electrical circuit model of a micro PV module [Lu 2010b], which is composed mainly of a current source and a forward biased diode.  $I_{\rm PH,SC}$  is the generated photocurrent by photovoltaic conversion,  $R_{\rm S}$  is the parasitic series resistance, and  $R_{\rm P}$  is the equivalent shunt resistance.  $I_{\rm PH}$  and  $V_{\rm PH}$  are the output current and terminal voltage of the PV module, respectively.

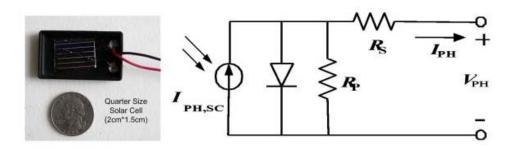


Figure 2. The electrical model of a PV module

Based on the circuit shown in Figure 2, the output current  $(I_{PH})$  and power  $(P_{PH})$  of a PV module can be expressed as

$$I_{\rm PH} = I_{\rm PH,SC} - I_{\rm SAT} \left\{ e^{\frac{q}{AKT}(V_{\rm PH} + I_{\rm PH}R_{\rm S})} - 1 \right\} - \frac{V_{\rm PH} + I_{\rm PH}R_{\rm S}}{R_{\rm P}} \quad P_{\rm PH} = I_{\rm PH}V_{\rm PH}$$
(1)

Here,  $I_{\text{SAT}}$  is the reverse saturation current, q is the electron charge, A is a dimensional factor, K is the Boltzmann constant, and T is the operating temperature. We conducted experiments using a commercial PV module (Model #1-100, SolarWorld Inc.) to validate this model. The PV module was characterized under weak light (indoor) conditions. The PV module was illuminated using a 40-Watt light bulb and the distance between them was adjusted to emulate changing light conditions. Various resistive loads were connected to the PV module and the output voltage and current were measured. Figure 3(a) plots the I-V curve of the PV module obtained using Eq. (1) and measured experimentally. We can see that the measured  $I_{PH}$  values fit well with the values predicted by the electrical model.

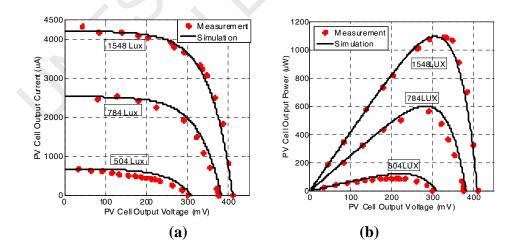


Figure 3. (a) Measured I-V characteristic of a PV module (b) Output power vs. PV terminal voltage of a PV module

Figure 3(b) plots the output power ( $P_{PH}$ ) of the PV module as a function of its terminal voltage. As is evident from the figure, for a given light irradiance, there exists an

optimal output voltage ( $V_{\rm MPP}$ ) for the PV module at which  $P_{\rm PH}$  is maximized (*e.g.*, 0.29V for 784*LUX*). This point on the *I*-V curve is the MPP. Note that the MPP changes significantly as the light intensity changes. The goal of MPP tracking schemes is to ensure that the PV module operates at its MPP at any given time. It can also be seen in Figure 3(b) that the harvested power is very limited (in the range of several hundred  $\mu$ W to 1.1mW). Obviously, we would like as much of this power as possible to be available to the load. Therefore, the power budget for an MPP tracking scheme in such a system is severely constrained (*e.g.*, at most a few  $\mu$ W), which requires the MPP tracking subsystem to be very carefully designed.

## 2.2 Micro Thermoelectric Generator

Micro TEGs are scalable, reliable and do not require any moving parts like vibration energy transducers. As a consequence, it is very appealing in micro scale energy harvesting systems, such as human body powered biomedical devices. Micro TEGs typically consist of multiple couples of p-type and n-type thermoelectric legs, which can output electrical energy by employing the temperature gradient between the hot surface (e.g., human body) and the cold surface (e.g., ambient air). These thermocouples are usually connected electrically in series and thermally in parallel to effectively make use of the limited surface area. When there is a temperature difference across a  $\mu$ TEG, seebeck effect causes the moving of charged carriers to generate a terminal voltage. Figure 4 illustrates the operation mechanism of a  $\mu$ TEG. The top layer of the  $\mu$ TEG is attached to a heat surface, while the bottom layer is placed near a cool surface. Due to the temperature difference, the electrons (or holes) in the N-type (or P-type) material flow towards the cool surface and forms a current.

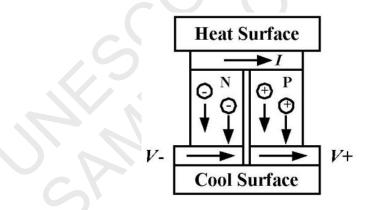


Figure 4. Illustration of operation mechanism of a µTEG

In [Egbert 2007], the figure of merit (FOM) of a micro TEG is defined as

$$Z = \frac{\alpha^2}{\lambda \rho} \tag{2}$$

Here  $\alpha$  is the seebeck coefficient that is material dependent,  $\lambda$  is the thermal conductivity, and  $\rho$  is the electrical resistivity. Improving the FOM from a device or material perspective is one area of active research in thermoelectric community.

Micropelt MPG-D751 is a good example of small scale TEG devices. The current and voltage values for different  $\Delta T$  across it were obtained by the simulation tool supported by the manufacturer and were plotted in Figure 5. The output power varies as a function of output voltage for different  $\Delta T$ . The maximum output power is maintained when its output voltage is around half of the open circuit voltages (i.e. 0.31V for  $\Delta T = 4K$ , 0.23V for  $\Delta T = 3K$ ). The open circuit voltage of a TEG is proportional to the number of leg pairs, the actual temperature difference  $\Delta T$  and the seebeck coefficient  $\alpha$ , as shown in the equation below:

$$V_{\rm OC} = \alpha \times N_{\rm LEGPAIRS} \times \Delta T \tag{3}$$

We can see that a TEG can be modeled as a voltage source in series with an internal resistor with the voltage source being proportional to  $\Delta T$ . Such a model can be expressed using Eqs. (4) and (5), where  $\beta$  is a constant (i.e., internal resistor).

(4)

(5)

$$V_{\rm TEG} = V_{\rm OC} - \beta I_{\rm TEG}$$

 $\mathbf{P}_{\mathrm{TEG}} = V_{\mathrm{TEG}} I_{\mathrm{TEG}} = V_{\mathrm{TEG}} (V_{\mathrm{OC}} - V_{\mathrm{TEG}}) / \beta$ 

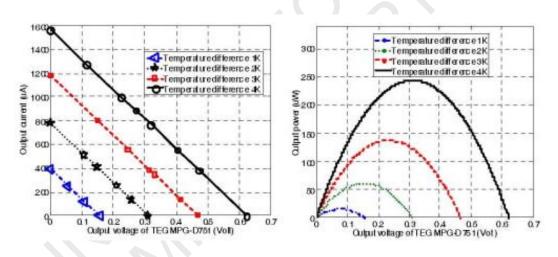


Figure 5. Simulation results of TEG MPG-D751 ( $\Delta T$ =1~4K)

## 2.3 Micro Fuel Cell

Micro Fuel cell ( $\mu$ FC) is a viable alternative power source that converts fuel energy into electrical energy by chemical reaction of a fuel in the presence of a catalyst.  $\mu$ FC is considered as a green power source because the outputs of chemical reaction are environmental clean. The fuel has a much higher energy density. For example, theoretically the energy density of a methanol is five times higher than that of a lithium ion battery. Thus, it can achieve longer lifetime for the same weight or volume. With the advance of cutting-edge MEMS technology, researchers can shrink the size of fuel cells to chip dimension and integrate it with an integrated circuit (IC) to form a system-in-package (SIP) platform [Torres 2008]. In [Chu 2006], a silicon-based chip-scale fuel cell is fabricated and measured. Figure 6 shows a typical *V-I* characteristic (solid line)

of a micro fuel cell. When the current density increases from zero, the  $\mu$ FC passes through three distinct operation regions: activation, ohmic and concentration polarization. Figure 6 also shows the estimated output power curve (dash line). It is obvious that there exists a maximum power point (MPP) in the region of ohmic polarization.

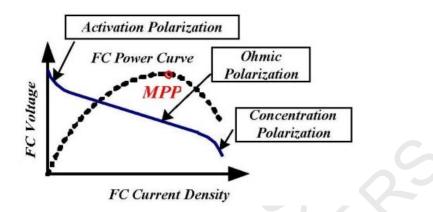


Figure 6. V-I and P-I characteristics of a micro scale fuel cell

Most existing fuel cell models assume constant fuel flow and unchanging concentration conditions [Yu 2004]. As a result, these models are only applicable to predict steady-state, time-independent behaviors. In [Chen 2008], a Cadence-compatible electrical model for a micro-scale direct methanol fuel cell (DMFC) was first developed to express the dynamic and steady state electrical behavior. This proposed electrical model is capable of prediction of runtime, large/small signal steady state or transient responses.

-

-

TO ACCESS ALL THE **26 PAGES** OF THIS CHAPTER, Visit: <u>http://www.eolss.net/Eolss-sampleAllChapter.aspx</u>

### **Bibliography**

Brunelli, D. (2008). An efficient solar energy harvester for wireless sensor nodes. In Proceedings of the Design, Automation, and Test in Europe (DATE) Conference, 104-109. [This presents a design using an additional pilot cell to sense the variation of light intensity].

Chen, M. (2007). Single inductor multiple input multiple output (SIMIMO) power mixer-charger-supply system. In Proceedings of the International Symposium on Low Power Electronics and Design (ISLPED), 310-315. [This proposes a design for multiple input multiple output power management purpose].

Chen, M. (2008). A compact electrical model for microscale fuel cells capable of predicting runtime and I-V polarization performance. *IEEE Trans. Energy Conversion*, 23, 842-850. [This paper investigates the development and verification of a compact electrical model of fuel cells].

Choi, W. J. (2006). Energy harvesting MEMS device based on thin film piezoelectric cantilevers. *Journal of Electroceramics*, 17, 543-548. [This is a case study of fabrication of a MEMS based thin film piezoelectric transducer].

Chu, K. L. (2006). A nanoporous silicon membrane electrode assembly for on-chip micro fuel cell applications. *Journal of Microelectromechanical Systems*, 15, 671-677. [It addresses how to design and fabricate on-chip fuel cell applications].

Clark, S. S. (2009). Towards autonomously-powered CRFIDs. In Workshop on Power Aware Computing and Systems (HotPower). [This work presents the design of hybrid energy harvesting RFID systems].

Dickson, J. (1976). On-chip high voltage generation in MNOS integrated circuits using an improved voltage multiplier technique. *IEEE J. of Solid State Circuits*, 1, 374-378. [It is the first work in the world about charge pump power converter].

Egbert, R. G. (2007). Microscale silicon thermoelectric generator with low impedance for energy harvesting. In Proceedings of 5th European Conference on Thermoelectrics, 219-221. [This work explains how to make a micro-scale thermoelectric geneator, which has a low impedance and especially suitable for energy harvesting].

Esram, A. E. (2007). Comparison of photovoltaic array maximum power point tracking techniques. *IEEE Trans on Energy Conversion*, 22, 439-449. [It is a comprehensive survey and review article about existing tracking techniques for large-scale photovoltaic cell].

Favrat, P. (1998). A high-efficiency CMOS voltage doubler. *IEEE J. of Solid State Circuits*, 33, 410-416. [This work presents a high efficient, cross coupled charge pump topology].

Ghosh, S. (2009). Voltage scalable high-speed robust hybrid arithmetic units using adaptive clocking. *IEEE Trans on Very Large Scale Integration (VLSI) Systems*, 18, 1301-1309. [This work introduces an adaptive clock stretching technique that achieves harvesting aware application unit].

Guo, S. (2009). An efficiency-enhanced CMOS rectifier with unbalanced-biased comparators for transcutaneous-powered high-current implants. IEEE J. of Solid-State Circuits, 44, 1796-1804. [This presents an efficiency-enhanced AC-DC rectifier for micro power biomedical implants].

Hsu, J. (2006). Adaptive duty cycling for energy harvesting systems. In Proceedings of the International Symposium on Low Power Electronics and Design (ISLPED), 180-185. [The authors propose an adaptive duty cycle adjustment technique for energy efficient energy harvesting systems].

Hwang, M. (2010). AERM: adaptive  $\beta$ -ratio modulation for process-tolerant ultra dynamic voltage scaling. *IEEE Trans on Very Large Scale Integration (VLSI) Systems*, 18, 281-290. [The authors address a process-tolerant ultra dynamic voltage scaling technique that balances the pull-up and pull-down].

Kansal, A. (2006). Harvesting aware power management for sensor networks. In Proceedings of the Design Automation Conference (DAC), 651-656. [This work mainly focuses on system level modeling and analysis of power management in wireless sensor networks].

Karakonstantis, G. (2009). Process-variation resilient and voltage-scalable DCT architecture for robust low-power computing. *IEEE Trans. on Very Large Scale Integration (VLSI) Systems*, 18(10), 1461-1470. [This work mainly focuses on how to build a energy aware digital signal processing system].

Karthaus, U. (2003). Fully integrated passive UHF RFID transponder IC with  $16.7-\mu$ W minimum RF input power. *IEEE J. of Solid State Circuits*, 38, 1602-1608. [Power conversion in RFID systems is a crucial design issue. This work explains how to design a multi-stage AC/DC power converter].

Kimball, J. W. (2004). Issues with low-input-voltage boost converter design. In Proceedings of Power Electronics Specialists Conference, 3, 2152- 2156. [This paper reviews the design challenge and issue of ultra low voltage energy harvesting, which is a very good survey work for circuit designers].

Lam, Y. H. (2006). Integrated low-loss CMOS active rectifier for wirelessly power devices. *IEEE Trans.* On Circuits and System II, 53, 1378-1382. [This work presents a rectifier that uses a reverse leakage current control technique].

Le, T. T. (2006). Piezoelectric micro-power generation interface circuits. *IEEE J. of Solid State Circuits*, 41, 1411-1420. [The authors investigate a low-cost high-efficient energy harvesting interface for micro power piezoelectric applications].

Le, T. T, (2008). Efficient far-field radio frequency energy harvesting for passively powered sensor networks. *IEEE J. of Solid-State Circuits*, 43, 1287-1302. [A floating gate technique is presented to result in a remarkable improvement in power conversion efficiency].

Lu, C. (2007). Vibration energy scavenging and management for ultra low power applications. In Proceedings of the International Symposium on Low Power Electronics and Design (ISLPED), 316-321. [The authors present a hybrid AC/DC rectifier which is capable of self starting up and has a power conversion efficiency as high as 90%].

Lu, C. (2010a). Maximum power point considerations for micro-scale solar energy harvesting systems. In Proceedings of IEEE International Symposium on Circuits and Systems (ISCAS), 273-276. [This work summaries existing maximum power point tracking approaches in micro-scale energy harvesting systems].

Lu, C. (2010b). Efficient power conversion for ultra low voltage micro scale energy transducers," In Proceedings of the Design, Automation, and Test in Europe (DATE) Conference. [A novel tree topology of charge pump is proposed and studied and it proves that at least 20% of energy harvesting efficiency can be achieved].

Lu, C. (2010c). Micro-scale energy harvesting: a system design perspective. In Proceedings of the IEEE Asia and South Pacific Design Automation Conference (ASPDAC). [This paper describes each system building block of a micro scale energy harvesting system and their individual design challenge].

Ma, D. (2010). Integrated interleaving SC power converters with analog and digital control schemes for energy-efficient microsystems. *Journal of Analog Integrated Circuits and Signal Processing*, 62(3), 361-372. [This work describes a charge pump power converter design for battery-based systems].

Mateu, L. (2005). Review of energy harvesting techniques and applications for microelectronics. In Proceedings of the SPIE Microtechnologies for the New Millennium, 359-373. [This work summarizes and reviews various common energy harvesting techniques in micro scale systems].

Mateu, L. (2007). Human body energy harvesting thermogenerator for sensing applications. In Proceedings of International Conference on Sensor Technologies and Applications, 366-372. [The authors present a designed human body thermal energy harvesting systems and provide experimental measurement results].

Meniger, S. (2001). Vibration-to-electric energy conversion. *IEEE Trans. on Very Large Scale Integration (VLSI) Systems*, 9, 64-76. [It discusses modeling, analysis, fabrication and measurement of electrostatic vibration energy harvesting].

MICROPELT, INC. (www.micropelt.com/)

MICROPELT SIMULATION TOOLS, (www.micropelt.com/products/mypelt.php)

Ottman, G. K. (2002). Adaptive piezoelectric energy harvesting circuit for wireless remote power supply. *IEEE Trans. on Power Electronics*, 17, 669-676. [Output power of piezoelectric energy transducer varies with time and position. The authors present a novel adaptive control scheme that can intelligently track the vibration status].

Raghunathan, V. (2004). Energy efficient design of wireless sensor nodes. in *Wireless Sensor Networks*, Kluwer Academic Publishers. [Almost every aspects of designing wireless sensor nodes are in-depth discussed].

Raghunathan, V. (2005). Design considerations for solar energy harvesting wireless embedded systems. In Proceedings of ACM/IEEE International Conference on Information Processing in Sensor Networks (IPSN), 457-462. [Design challenges and considerations for micro scale solar energy harvesting systems are reviewed].

Pan, F. (2006). Charge Pump Circuit Design, McGraw-Hill Professional.

Park, C. (2006). AmbiMax: autonomous energy harvesting platform for multi-supply wireless sensor nodes. In Proceedings of IEEE Communications Society on Sensor and Ad Hoc Communications and Networks, 168-177. [A hybrid energy harvesting system is designed and demonstrated. Solar and wind energy are collected at the same time].

Park, J. (2010). Dynamic bit-width adaptation in DCT: an approach to trade off image quality and computation energy. *IEEE Trans. on Very Large Scale Integration (VLSI) Systems.* 18, 787-793. [This technique can be used in energy aware energy harvesting systems, which may use dynamic bit width to tradeoff between quality and power].

Roundy, S. (2003). A study of low level vibrations as a power source for wireless sensor nodes, *Computer Communications*, 1131-1144. [Many common vibration sources are measured and characterized in this paper].

Shao, H. (2007). A micro power management system and maximum output power control for solar energy harvesting applications. In Proceedings of the International Symposium on Low Power Electronics and Design (ISLPED), 298-303. [A hill-climbing tracking algorithm and custom-designed hardware is presented and investigated].

Shao, H. (2009a). A single inductor dual input dual output DC-DC converter with hybrid supplies for solar energy harvesting applications. In Proceedings of the International Symposium on Low Power Electronics and Design (ISLPED), 69-74. [A band-to-band control algorithm and circuit implementation is presented for multi-input multi-output systems].

Shao, H. (2009b). The design of a micro power management system for applications using photovoltaic cells with the maximum output power control. *IEEE Trans. on Very Large Scale Integration (VLSI) Systems*, 17, 1138-1142. [This is an extended version of a conference paper. Experimental waveforms are included to validate its functionality].

Soeleman, H. (2001). Robust subthreshold logic for ultra-low power operation. *IEEE Trans. on Very Large Scale Integration (VLSI) Systems*, 9, 90-99. [An idea of operating electronic system in subthreshold mode is explored in this paper].

Solar World Inc., (www.solarworld.com)

Su, F. (2005). Gate control strategies for high efficiency charge pumps. In Proceedings of IEEE International Symposium on Circuits and Systems (ISCAS), 1907-1910. [A novel gate control strategy is proposed to improve charge pump power efficiency].

Sze, N. M. (2008). Integrated single-inductor dual-input dual-output boost converter for energy harvesting applications. In Proceedins of IEEE International Symposium on Circuits and Systems (ISCAS), 2218-2221. [In this work, a single inductor dual-output DC-DC boost converter is designed and fabricated for energy harvesting systems].

Torres, E. O. (2008). Energy-harvesting system-in-package (SiP) microsystem. *ASCE Journal of Energy Engineering*, 134, 121-129. [The authors investigate how to make compact cost-effective system-in-package micro systems].

Tsui, C.Y. (2006). Ultra-low voltage power management circuit and computation methodology for energy harvesting applications. In Proceedings of the 11th Asia and South Pacific Design Automation Conference (ASPDAC), 96-97. [A computation methodology about how to operate energy harvesting applications is explored].

Yazicioglu, R. F. (2009). Ultra-low-power biopotential interfaces and their applications in wearable and implantable systems. *Microelectronics Journal*. Vol. 40, 1313-1321. [The work addresses design requirements and challenges for wearable and implantable systems].

Yi, J. (2007). Analysis and design strategy of UHF micro-power CMOS rectifiers for micro-sensor and RFID applications. *IEEE Trans. On Circuits and Systems I*, 54, 153-166. [The authors develop a design

and analysis methodology for RFID micro power systems. This methodology is extremely useful in practice].

Ying, T. (2003). Area-efficient CMOS charge pumps for LCD drivers. *IEEE J. of Solid State Circuits*, 38, 1721-1725. [The authors would like to reduce the area of charge pump circuitry for LCD applications. The proposed circuit is optimized in terms of design area].

Yu, D. (2004). A novel circuit model for PEM fuel cells. In Proceedings of 2004 IEEE Applied Power Electronics Conference and Exposition, 1, 362-366. [Fuel cell behavior is modeled with the assumption of constant fuel flow and concentration condition].

#### **Biography Sketches**

**Chao Lu** received the B.S. degree in electrical engineering from the Nankai University, Tianjin, China and the M.S. degree in the Department of Electronic and Computer Engineering from the Hong Kong University of Science and Technology, Hong Kong, in 2004 and 2007, respectively. Since 2008, he has been pursuing the Ph.D. degree at Purdue University, West Lafayette, Indiana, USA.

His research interests include design and analysis of micro-scale energy harvesting systems, power management integrated circuits for ultra low power applications, and device and architecture co-design of low temperature poly silicon thin film transistors. Mr. Lu was the recipient of the Best Paper Award of the International Symposium on Low Power Electronics and Design (2007).

**Vijay Raghunathan** received the B.Tech. degree in Electrical Engineering from the Indian Institute of Technology, Madras, in 2000, and the M.S. and Ph.D. degrees in Electrical Engineering from the University of California, Los Angeles, in 2002 and 2006, respectively.

He is currently an Assistant Professor in the School of Electrical and Computer Engineering at Purdue University, where he leads the Embedded Systems Lab. Prior to joining Purdue, he was a visiting researcher at NEC Laboratories America in Princeton, NJ from August 2005 to August 2006. His research interests include the design of embedded computing systems, system-on-chip architectures, and wireless sensor networks with an emphasis on low power design and reliable system design. He has co-authored a book chapter, several journal and conference papers, and has presented full-day and embedded tutorials on the above topics. He serves on the organizing and technical program committees of several leading ACM and IEEE conferences in the areas of embedded systems, VLSI design, and wireless sensor networks.

Vijay is a recipient of an NSF CAREER award, the Edward K. Rice Outstanding Doctoral Student Award from the UCLA School of Engineering and Applied Sciences, and the Outstanding Masters Student Award from the UCLA Electrical Engineering Department. He also received the design contest award at the ACM/IEEE International Symposium on Low Power Electronics and Design in 2005, the best student paper award at the IEEE International Conference on VLSI Design in 2000, and a best paper award nomination at the ACM/IEEE International Symposium on Low Power Electronics and Design in 2006.

**Kaushik Roy** received B.Tech. degree in electronics and electrical communications engineering from the Indian Institute of Technology, Kharagpur, India, and Ph.D. degree from the electrical and computer engineering department of the University of Illinois at Urbana-Champaign in 1990. He was with the Semiconductor Process and Design Center of Texas Instruments, Dallas, where he worked on FPGA architecture development and low-power circuit design. He joined the electrical and computer engineering faculty at Purdue University, West Lafayette, IN, in 1993, where he is currently a Professor and holds the Roscoe H. George Chair of Electrical & Computer Engineering. His research interests include Spintronics, VLSI design/CAD for nano-scale Silicon and non-Silicon technologies, low-power electronics for portable computing and wireless communications, VLSI testing and verification, and reconfigurable computing. Dr. Roy has published more than 500 papers in refereed journals and conferences, holds 15 patents, graduated 50 PhD students, and is co-author of two books on Low Power CMOS VLSI Design (John Wiley & McGraw Hill).

Dr. Roy received the National Science Foundation Career Development Award in 1995, IBM faculty partnership award, ATT/Lucent Foundation award, 2005 SRC Technical Excellence Award, SRC Inventors Award, Purdue College of Engineering Research Excellence Award, Humboldt Research

Award in 2010, and best paper awards at 1997 International Test Conference, IEEE 2000 International Symposium on Quality of IC Design, 2003 IEEE Latin American Test Workshop, 2003 IEEE Nano, 2004 IEEE International Conference on Computer Design, 2006 IEEE/ACM International Symposium on Low Power Electronics & Design, and 2005 IEEE Circuits and system society Outstanding Young Author Award (Chris Kim), 2006 IEEE Transactions on VLSI Systems best paper award. Dr. Roy is Purdue University Faculty Scholar. He was a Research Visionary Board Member of Motorola Labs (2002) and held the M.K. Gandhi Distinguished Visiting faculty at Indian Institute of Technology (Bombay). He has been in the editorial board of IEEE Design and Test, IEEE Transactions on Circuits and Systems, and IEEE Transactions on VLSI Systems. He was Guest Editor for Special Issue on Low-Power VLSI in the IEEE Design and Test (1994) and IEEE Transactions on VLSI Systems (June 2000), IEEE Proceedings -- Computers and Digital Techniques (July 2002). Dr. Roy is a fellow of IEEE.