

DESIGN METHODS FOR DIGITAL CONTROLLERS, SAMPLE-RATE

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Summary

There are several methods for designing a digital controller. One method is to discretize the existing continuous-time controller. The most popular method is to design the digital controller directly by extending all known continuous-time design methods to the case of discrete-time systems. Following this approach, we present digital controller design methods via root locus, Bode diagram, and Nyquist diagrams. A brief discussion is presented with regard to the PID controllers, state-space design methods, and optimal control for discrete-time systems.

In discretizing continuous-time systems the sample rate is of great concern, since it may seriously affect the performance of the closed-loop system. This is demonstrated by varying the sampling rate and observing its strong influence upon the root locus of the closed-loop system.

1. Design Methods for Digital Controllers

1.1. Introduction

The classical discrete-time controller design methods are distinguished into indirect and direct techniques.

Indirect techniques: Using these techniques, a discrete-time controller $G_c(z)$ is determined indirectly as follows. Initially, the continuous-time controller $G_c(s)$ is designed in the s-domain, using well-known classical techniques (e.g. root-locus, Bode, Nyquist). Then, based on the continuous-time controller $G_c(s)$, the discrete-time controller $G_c(z)$ may be calculated using one of the discretization techniques presented in article *Discrete-Time Equivalents to Continuous Time Systems*. The indirect techniques are presented in Section 1.2.

Direct techniques: These techniques start by deriving a discrete-time mathematical model of the continuous-time system under control. Subsequently, the design is carried out in the z-domain, wherein the discrete-time controller $G_c(z)$ is directly determined. The design in the z-domain may be done either using the root-locus method (see Section 1.3) or the Bode and Nyquist diagrams (see Section 1.4). Special attention is given to PID discrete-time controller design (Section 1.5). This three-term controller is most popular in industrial applications.

1.2. Discrete-Time Controller Design Using Indirect Techniques

The practicing control engineer often has greater knowledge and experience in designing continuous-time than discrete-time controllers. Moreover, many practical systems already incorporate a continuous-time controller that we desire to replace with a discrete-time controller. The remarks above are the basic motives for the implementation of indirect design techniques for discrete-time controllers mentioned in Section 1.1. Indirect techniques take advantage of the knowledge and the experience the designer has for continuous-time systems. Furthermore, in cases where a continuous-time controller is already incorporated in the system under control, it facilitates the design of a discrete-time controller. Consider the continuous-time closed-loop control system shown in Figure 1 and the discrete-time closed-loop control system shown in Figure 2. The indirect design technique for the design of a discrete-time controller may be stated as follows. Let the specifications of the closed-loop systems shown in Figures 1 and 2 be the same. Assume that a continuous-time controller $G_c(s)$, satisfying the specifications of the closed-loop system shown in Figure 1, has already been determined. Then, the discrete-time controller $G_c(z)$ shown in Figure 2 may be calculated from the continuous-time controller of Figure 1, using the discretization techniques presented in article *Discrete-Time Equivalents to Continuous Time Systems*.

It is remarked that in replacing a continuous-time controller by a digital controller, a zero-order hold (ZOH) is introduced. This causes additional phase lag, a fact, which influences the closed-loop system performance.

1.3. Direct Digital Controller Design via the Root-Locus Method

The root-locus method is a direct method for determining $G_c(z)$ and is applied as follows. Consider the closed-loop system shown in Figure 3. The transfer function $H(z)$ of the closed-loop system is

$$H(z) = \frac{G(z)}{1 + G(z)F(z)} \quad (1)$$

The characteristic equation of the closed-loop system is

$$1 + G(z)F(z) = 0 \quad (2)$$

For linear time-invariant systems, the open-loop transfer function $G(z)F(z)$ has the form

$$G(z)F(z) = K \frac{\prod_{i=1}^m (z + z_i)}{\prod_{i=1}^n (z + p_i)} \quad (3)$$

Substituting (3) in (2) yields the algebraic equation

$$\prod_{i=1}^n (z + p_i) + K \prod_{i=1}^m (z + z_i) = 0 \quad (4)$$

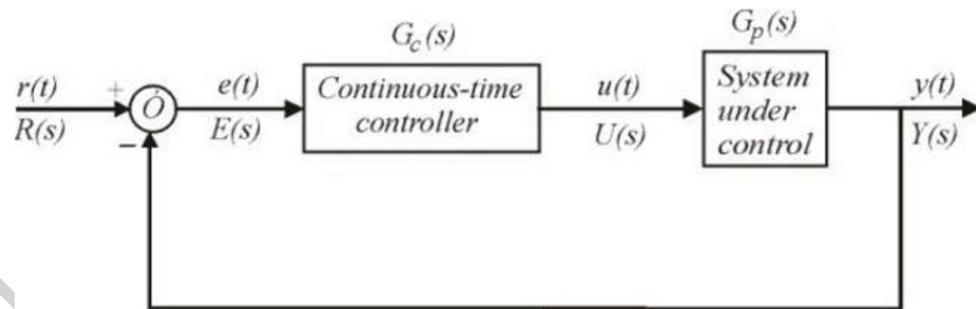


Figure 1. Continuous-time closed-loop system

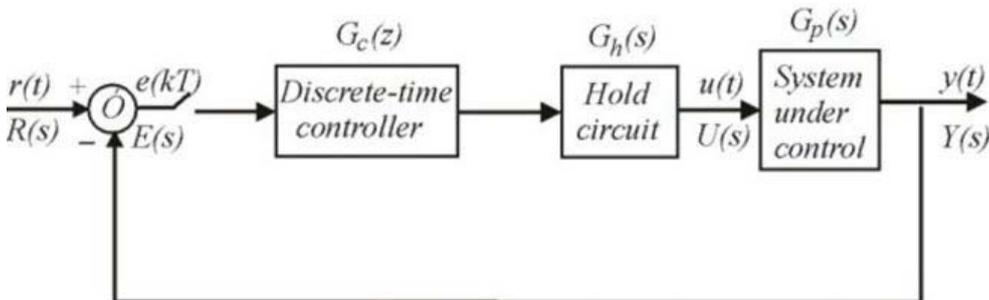


Figure 2. Discrete-time closed-loop control system

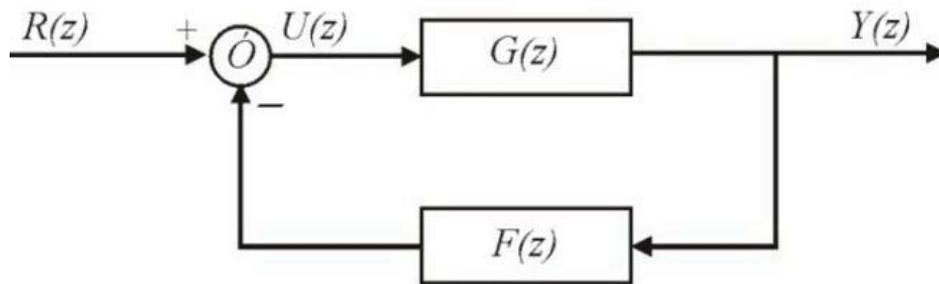


Figure 3. Discrete-time closed-loop system

Definition 1: The root-locus of the closed-loop system of Figure 3 is the locus of (4) in the z -domain as the parameter K varies from $-\infty$ to $+\infty$. Since the poles $-p_i$ and the zeros $-z_i$ are, in general, functions of the sampling time T , it follows that for each T there corresponds a root-locus of (4), thus yielding a family of root-loci for various values of T .

The root-locus of (4) is constructed using the well-known simple root-locus rules.

1.4. Direct Digital Controller Design Based on the Frequency Response

1.4.1. Introduction

The well-established frequency domain controller design techniques for continuous-time systems can be extended to cover the case of discrete-time systems. At first, one might think of carrying out this extension by using the relation $z = e^{sT}$. Making use of this relation, the simple and easy to use logarithmic curves of the Bode diagrams for the continuous-time case cease to hold for discrete-time systems (that is why the extension via relation $z = e^{sT}$ is not recommended). To maintain the simplicity of the logarithmic curves for the discrete-time systems, we make use of the following bilinear transformation

$$z = \frac{1 + Tw/2}{1 - Tw/2} \text{ or } w = \frac{2}{T} \left[\frac{z - 1}{z + 1} \right] \quad (5)$$

The transformation of a function of s to a function of z based on the relation $z = e^{sT}$, and subsequently the transformation of the resulting function of z to a function of w based on Eq. (5), are presented in Figure 4. The figure shows that the transformation of the left-half complex plane on the s -plane transforms into the unit circle in the z -plane via the relation $z = e^{sT}$, whereas the unit circle on the z -plane transforms into the left-half complex plane in the w -plane, via the bilinear transformation of Eq. (5).

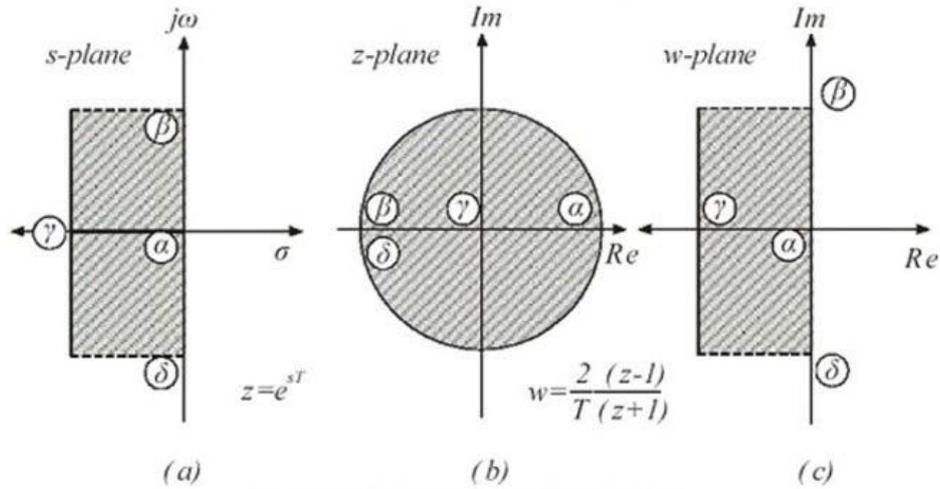


Figure 4. Mappings from the s-plane to z-plane and from z-plane to w-plane

At first sight, it seems that the frequency responses would be the same in both the s and the w domain. This is actually true, with the only difference that the scales of the frequencies w and v are distorted, where v is the (hypothetical or abstract) frequency in the w -domain. This frequency “distortion” may be observed if in Eq. (5) we set $w = jv$ and $z = e^{j\omega T}$, yielding

$$w \Big|_{w=jv} = jv = \frac{2}{T} \left[\frac{z-1}{z+1} \right] \Big|_{z=e^{j\omega T}}$$

$$= \frac{2}{T} \left[\frac{e^{j\omega T} - 1}{e^{j\omega T} + 1} \right] = j \frac{2}{T} \tan \frac{\omega T}{2}$$

Therefore

$$v = \frac{2}{T} \tan \frac{\omega T}{2} \tag{6}$$

Since

$$\tan \frac{\omega T}{2} = \frac{\omega T}{2} - \frac{(\omega T)^3}{8} + \dots \tag{7}$$

it follows that for small values of ωT we have that $\tan \frac{\omega T}{2} \cong \frac{\omega T}{2}$. Substituting this result in equation (6) we have

$$v \cong \omega, \text{ for small } \omega T \tag{8}$$

Therefore, the frequencies ω and v are linearly related if the product ωT is small. For

greater ωT , Eq. (8) does not hold true. Figure 5 shows the graphical representation of Eq. (6). It is noted that the frequency range $-\omega_s/2 \leq \omega \leq \omega_s/2$ in the s-domain corresponds to the frequency range $-\infty \leq \nu \leq \infty$ in the w-domain, where ω_s is defined by the relation $(\omega_s/2)(T/2) = \pi/2$.

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Biographical Sketch

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